



Synaptics General ESD/EOS Control Methods Application Note

PN: 506-001454-01 Rev B

Downloaded by Anonymous () on 21 Jun 2023 06:47:37 UTC

Contents

- 1. ESD/EOS Control for Safe Environment 3
 - 1.1. Risk at IC Handlers 3
 - 1.2. Risks Occur During Pick and Place..... 3
- 2. Basic ESD/EOS Control Methods 4
 - 2.1. Manufacturing Area ESD Control 4
 - 2.2. Product/Application Level 4
- 3. Revision History..... 5

Downloaded by Anonymous () on 21 Jun 2026 06:47:37 UTC

1. ESD/EOS Control for Safe Environment

A safe electrostatic overstress (EOS) / electrostatic discharge (ESD) environment is one where there is no EOS/ESD exposure to sensitive environments. This requires a comprehensive approach.

- The most ionizers
- The best ground
- Everyone wears wrist straps
- Compliance with ESD standards

1.1. Risk at IC Handlers

Typically, discharges occur when IC is managed under the following situations:

- is picked up from the input tray
- is placed in the input shuttle
- is placed in the test socket
- is lifted from the test socket
- is placed in the exit shuttle
- is placed in the output tray

It is important to configure the air ionizers to eliminate static electricity while handling the IC. The number of ionizers should be sufficient.

Personnel should wear grounding straps while moving the tray.

1.2. Risks Occur During Pick and Place

Personnel grounding straps are necessary while moving the tray (during tray in/out). Air ionizer configuration on the machine is required. There should be cart and shelving grounding checks, and a power outage record check.

2. Basic ESD/EOS Control Methods

2.1. Manufacturing Area ESD Control

The following protections should be in place to control ESD in the manufacturing area:

- Use of conductive flooring and workspaces
- Use of personnel grounding straps
- Cart and shelving grounding
- Ionizer configuration for IC handlers and FT machine
- Checking of solder rework equipment for EM leakage
- Control of humidity levels in work areas

2.2. Product/Application Level

- Clean VDD/VSS supplies
- Controlled VDD ramp at power up and power down
- Proper power/ground de-coupling capacitors
- PCB low-resistance conduction paths for power and ground

Downloaded by Anonymous () on 21 Jun 2026 06:47:37 UTC

3. Revision History

Revision	Description
A	Initial release.
B	Released as a public document.

Copyright

Copyright © 2024 Synaptics Incorporated. All Rights Reserved.

Trademarks

Synaptics and the Synaptics logo are trademarks or registered trademarks of Synaptics Incorporated in the United States and/or other countries.

All other trademarks are the properties of their respective owners.

Notice

Use of the materials may require a license of intellectual property from a third party or from Synaptics. This document conveys no express or implied licenses to any intellectual property rights belonging to Synaptics or any other party. Synaptics may, from time to time and at its sole option, update the information contained in this document without notice.

INFORMATION CONTAINED IN THIS DOCUMENT IS PROVIDED "AS-IS," WITH NO EXPRESS OR IMPLIED WARRANTIES, INCLUDING ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE, AND ANY WARRANTIES OF NON-INFRINGEMENT OF ANY INTELLECTUAL PROPERTY RIGHTS. IN NO EVENT SHALL SYNAPTICS BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, PUNITIVE, OR CONSEQUENTIAL DAMAGES ARISING OUT OF OR IN CONNECTION WITH THE USE OF THE INFORMATION CONTAINED IN THIS DOCUMENT, HOWEVER CAUSED AND BASED ON ANY THEORY OF LIABILITY, WHETHER IN AN ACTION OF CONTRACT, NEGLIGENCE OR OTHER TORTIOUS ACTION, AND EVEN IF SYNAPTICS WAS ADVISED OF THE POSSIBILITY OF SUCH DAMAGE. IF A TRIBUNAL OF COMPETENT JURISDICTION DOES NOT PERMIT THE DISCLAIMER OF DIRECT DAMAGES OR ANY OTHER DAMAGES, SYNAPTICS' TOTAL CUMULATIVE LIABILITY TO ANY PARTY SHALL NOT EXCEED ONE HUNDRED U.S. DOLLARS..

Contact Us

Visit our website at www.synaptics.com to locate the Synaptics office nearest you.

